

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
4 March 2004 (04.03.2004)

PCT

(10) International Publication Number
WO 2004/019392 A2

(51) International Patent Classification⁷: **H01L 21/203**

(21) International Application Number:
PCT/JP2003/010420

(22) International Filing Date: 18 August 2003 (18.08.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
0219729.1 23 August 2002 (23.08.2002) GB

(71) Applicant (for all designated States except US): **SHARP KABUSHIKI KAISHA** [JP/JP]; 22-22, Nagaikecho, Abeno-ku, Osaka-shi, Osaka 545-8522 (JP).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **BOUSQUET, Valerie** [FR/GB]; The Top Flat, Manor House, Henley Road, Sandford on Thames, Oxford OX4 4YN (GB). **HOOPER, Stewart Edward** [GB/GB]; 21 Thorne Close, Kidlington, Oxfordshire OX5 1SJ (GB). **BARNES, Jennifer Mary** [GB/GB]; 13 Larkfields, Headington, Oxford OX3 8PF (GB). **HEFFERNAN, Jonathan** [IE/GB]; 33 Denton Close, Oxford OX2 9BW (GB).

(74) Agents: **YAMAMOTO, Shusaku** et al.; Fifteenth Floor, Crystal Tower, 2-27, Shiromi 1-chome, Chuo-ku, Osaka-shi, Osaka 540-6015 (JP).

(81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

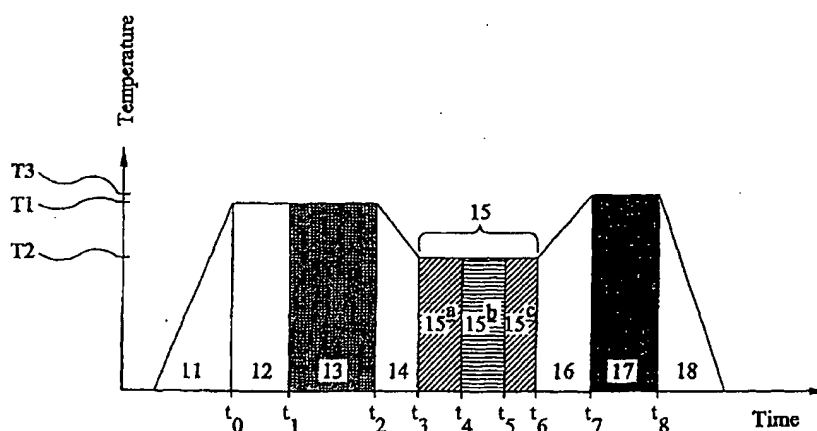
(84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— without international search report and to be republished upon receipt of that report

[Continued on next page]

(54) Title: MBE GROWTH OF A SEMICONDUCTOR LAYER STRUCTURE



(57) Abstract: A method of MBE growth of a semiconductor layer structure comprises growing a first (Al,Ga)N layer (step 13) over a substrate at the first substrate temperature (T1) using ammonia as the nitrogen precursor. The substrate is then cooled (step 14) to a second substrate temperature (T2) which is lower than the first substrate temperature. An (In,Ga)N quantum well structure is then grown (step 15) over the first (Al,Ga)N layer by MBE using ammonia as the nitrogen precursor. The supply of ammonia to the substrate is maintained continuously during the first growth step, the cooling step, and the second growth step. After completion of the growth of the (In,Ga)N quantum well structure, the substrate may be heated to a third temperature (T3) which is greater than the second substrate temperature (T2). A second (Al,Ga)N layer is then grown over the (In,Ga)N quantum well structure (step 17).

WO 2004/019392 A2

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

WO 2004/019392

4/18/05

- 1 -

PCT/JP2003/010420

10/525499
BT01 Rec'd PCT/PTC 23 FEB 2005

DESCRIPTION

MBE growth of a semiconductor layer structure

5

TECHNICAL FIELD

This invention relates to a molecular beam epitaxy (MBE) method for the epitaxial growth of Group III nitride semiconductor materials. It particularly relates to the epitaxial growth of a semiconductor layer structure, for example a quantum well or multiple quantum well structure, comprising one or more $\text{In}_x\text{Ga}_{1-x}\text{N}$ ($0 \leq x \leq 1$) layers. It also relates to the growth of a light-emitting diode containing one or more quantum wells.

15

BACKGROUND ART

The $\text{In}_x\text{Ga}_{1-x}\text{N}$ ($0 \leq x \leq 1$) material family will be referred to herein as "(In,Ga)N" for convenience. The term "InGaN" will be used to denote a member of the (In,Ga)N family having an indium mole fraction that is non-zero, but that is less than one.

20

The term "AlGaN" is used to denote a member of the

WO 2004/019392

PCT/JP2003/010420

- 2 -

$\text{Al}_y\text{Ga}_{1-y}\text{N}$ ($0 \leq y \leq 1$) material family having an aluminium mole fraction y that is non-zero but is less than 1. The complete material family, including $y = 0$ and $y = 1$, will be referred to herein as "(Al,Ga)N" for convenience.

5

The epitaxial growth of Group III nitride semiconductor materials on a substrate can be effected by molecular beam epitaxy (MBE) or by chemical vapour deposition (CVD) which is sometimes known as Vapour Phase Epitaxy (VPE).

10

CVD/VPE takes place in an apparatus which is commonly at atmospheric pressure but sometimes at a slightly reduced pressure of typically about 10 kPa. Ammonia and the species providing one or more Group III elements to be used in epitaxial growth are supplied substantially parallel to the surface of a substrate upon which epitaxial growth is to take place, thus forming a boundary layer adjacent to and flowing across the substrate surface. It is in this gaseous boundary layer that decomposition to form nitrogen and the other elements to be epitaxially deposited takes place so that the epitaxial growth is driven by gas phase equilibria.

20

In contrast to CVD/VPE, MBE is carried out in a high vacuum environment. In the case of MBE as applied to the

WO 2004/019392

PCT/JP2003/010420

- 3 -

(In,Ga)N system, an ultra-high vacuum (UHV) environment, typically around 1×10^{-3} Pa, is used. A nitrogen precursor is supplied to the MBE chamber by means of a supply conduit and species providing gallium and/or indium, and if desired also a suitable dopant species, are supplied from appropriate sources within heated effusion cells fitted with controllable shutters to control the amounts of the species supplied into the MBE chamber during the epitaxial growth period. The shutter-control outlets from the effusion cells and the nitrogen supply conduit face the surface of the substrate upon which epitaxial growth is to take place. The nitrogen precursor and the species supplied from the effusion cells travel across the MBE chamber and reach the substrate where epitaxial growth takes place in a manner which is driven by the deposition kinetics.

At present, the majority of growth of high quality nitride semiconductor layers is carried out using the metal-organic vapour phase epitaxy (MOVPE) process. The MOVPE process allows growth to occur at a V/III ratio well in excess of 1000:1. The V/III ratio is the molar ratio of the group V element to the Group III element during the growth process. A high V/III ratio is preferable, since this allows a higher substrate temperature to be used which in turn leads

WO 2004/019392

PCT/JP2003/010420

- 4 -

to a higher quality semiconductor layer.

At present, growing high quality nitride semiconductor layers by MBE is more difficult than growing
5 such layers by MOVPE. The principal difficulty is in supplying sufficient nitrogen during the growth process, and it is difficult to obtain a V/III ratio of 10:1 or greater during MBE growth of a nitride semiconductor layer. The two
10 commonly used sources of nitrogen in the MBE growth of nitride layers are plasma excited molecular nitrogen or ammonia.

One particular application of InGaN layers is in the manufacture of light-emitting diodes ("LEDs") and laser diodes that emit light in the blue region of the spectrum.
15 These are of use in many applications such as, for example, full-colour LED displays, full-colour photocopying machines or scanners, traffic lights and other devices requiring a full-colour light source. Light-emitting diodes and laser diodes that emit in the blue region of the spectrum can be
20 fabricated using layer structures of group III-nitride semiconductors. (In,Ga)N quantum well structures are an essential component in the active region of these light-emitting diodes and laser diodes.

WO 2004/019392

PCT/JP2003/010420

- 5 -

Acknowledgement of Prior Art

The (In,Ga)N quantum well structures in commercially available light-emitting diodes or laser diodes that emit
5 in the blue region of the spectrum are grown using the MOVPE growth technique. There is extensive prior art relating to the MOVPE growth conditions, and to the electrical and optical properties of light-emitting diodes grown by MOVPE. The commercial growth of such laser diodes or light-emitting
10 diodes by MOVPE has been reported by S. Nakamura et al in "Japanese Journal of Applied Physics" Vol. 34, pL1332 (1995), and by P.Kozodoy et al in "Gallium Nitride and Related Materials II Material Research Society, Symposium Proceedings" pp 481-486 (1997).

15

In contrast to MOVPE growth, there are only a few reports on the growth of InGaN-based LEDs by the MBE growth process using ammonia as the source of nitrogen.

20

M Mayer et al have reported, in "Journal of Crystal Growth" Vol. 201, p318 (1999), the growth of nitride-based LEDs using MBE with ammonia as the nitrogen source. However, the structure grown in this report is a simple p:n junction, and does not contain an InGaN quantum well structure.

WO 2004/019392

PCT/JP2003/010420

- 6 -

The growth of InGaN quantum well structures by MBE has been reported by H. Riechert et al in "Material Research Society Symposium Proceedings", Vol. 449, p149 (1997) and
5 by N. Grandjean et al in "Applied Physics Letters" Vol. 74 p3616 (1999). However, in both cases the growth temperature was low, being in the range 570-620°C. A low growth temperature was obtained because the flux of ammonia to the growth chamber was low, leading to a low V/III ratio. As
10 a consequence of the low growth temperature, the quality of the InGaN layers grown by these prior art MBE methods is much lower than the quality of InGaN layers grown by MOCVD.

US patent No 5 602 418 discloses a method of growing
15 a multi-crystalline layer on a substrate, so as to allow a single crystal layer that is not lattice-matched to the substrate to be grown over the multi-crystalline layer. The method is described for a number of material systems, including the growth of an InGaN layer over an R-face sapphire
20 substrate. It does not address the subsequent growth of further layers over the single crystal layer.

US patent No. 5 684 309 disclose a method of growing AlGaN/InGaN or AlInGaN/In GaN semiconductor layer structures

WO 2004/019392

PCT/JP2003/010420

- 7 -

by MOCVD.

DISCLOSURE OF THE INVENTION

5 A first aspect of the present invention provides a method of growing a semiconductor layer structure, the method comprising the steps of:

 growing a first (Al,Ga)N layer over a substrate at the first substrate temperature by MBE using ammonia as the
10 nitrogen precursor;

 cooling the substrate to a second substrate temperature lower than the first substrate temperature, while maintaining the supply of ammonia to the substrate;

 growing an (In,Ga)N quantum well structure over the
15 first (Al,Ga)N layer by MBE using ammonia as the nitrogen precursor;

 heating the substrate to a third substrate temperature higher than the second substrate temperature, while maintaining the supply of ammonia to the substrate;
20 and

 growing a second (Al,Ga)N layer over the quantum well structure at the third substrate temperature by MBE using ammonia as the nitrogen precursor.

WO 2004/019392

PCT/JP2003/010420

- 8 -

The (In,Ga)N quantum well structure contains one or more (In,Ga)N layers. It may contain, for example, at least two adjacent (In,Ga)N layers having different indium mole fractions.

5

The first (Al,Ga)N layer may have a first conductivity type. The second (Al,Ga)N layer may have a second conductivity type different from the first conductivity type. The first (Al,Ga)N layer may be doped n-type and the second (Al,Ga)N layer may be doped p-type.

10

The first substrate temperature may be within the range 850°C to 1050°C. The second substrate temperature may be within the range 650°C to 1000°C. The third substrate temperature may be within the range 850°C to 1050°C.

15

A second aspect of the present invention provides a semiconductor layer structure grown by a method as defined above.

20

A third aspect of the present invention provides a semiconductor light-emitting device comprising a semiconductor layer structure grown by a method as defined above. The device may be a light-emitting diode.

WO 2004/019392

PCT/JP2003/010420

- 9 -

BRIEF DESCRIPTION OF THE DRAWINGS

Preferred embodiments of the present invention will
5 now be described by way of an illustrative example with
reference to the accompanying figures in which:

Figure 1 is a schematic sectional view of a
light-emitting diode incorporating an (In,Ga)N quantum well
10 structure;

Figure 2 illustrates the relation between substrate
temperature and time for a growth method of the present
invention;

15

Figure 3 illustrates the electro-luminescence
spectrum for a LED grown according to a method of the present
invention; and

20

Figure 4 is a schematic view of an MBE growth apparatus
suitable for carrying out a method of the invention.

BEST MODE FOR CARRYING OUT THE INVENTION

WO 2004/019392

PCT/JP2003/010420

- 10 -

Figure 1 is a schematic illustration of a semiconductor layer structure. Figure 1 shows a light-emitting device, in this case a light-emitting diode or "LED", that emits light in the blue region of the spectrum.

5 The LED 1 has a substrate 2. The substrate could be a bulk substrate of GaN or $\text{Al}_y\text{Ga}_{1-y}\text{N}$ ($0 \leq y \leq 1$). The substrate could alternately consist of an epitaxial layer of GaN disposed on a base substrate of, for example, sapphire, SiC, Si, ZnO, or MgO.

10

The light emitting diode 1 of Figure 1 further comprises a multiple quantum well structure 4 in which light is, in use, generated. The multiple quantum well structure 4 consists of a plurality of (In,Ga)N layers having at least two different indium mole fractions. In the structure shown in Figure 1 the first (In,Ga)N layer 4a of the multiple quantum well structure 4 is a GaN layer or is an InGaN layer with a low indium mole fraction, preferably an indium mole fraction in the range from 0 to 0.05. The second (In,Ga)N layer 4b is an InGaN layer having a greater indium mole fraction than the first (In,Ga)N layer 4a, preferably within the range 0.05 to 0.2. The third (In,Ga)N layer 4c is a GaN layer, or an InGaN layer having a lower indium mole fraction than the second layer 4b. Preferably, the lower (In,Ga)N layer

15

20

WO 2004/019392

PCT/JP2003/010420

- 11 -

4a and the upper (In,Ga)N layer 4c have approximately the same indium mole fraction as one another. (The lower (In,Ga)N layer 4a and the upper (In,Ga)N layer 4c may each have an indium mole fraction of zero, in which case they are both GaN layers.)

The multiple quantum well region 4 shown in Figure 1 comprises three (In,Ga)N layers, but the multiple quantum well region 4 may consist of more than three layers if desired.

The multiple quantum well structure 4 is disposed between first and second (Al,Ga)N layers 3, 5. One or both of the (Al,Ga)N layers 3, 5 may be doped using a suitable impurity so as to display extrinsic conductivity. The two (Al,Ga)N layers 3, 5 are preferably doped so as to be of different conductivity types from one another. In the device shown in Figure 1 the lower (Al,Ga)N layer 3 is doped to have n-type conductivity and the upper (Al,Ga)N layer is doped to have p-type conductivity but, in principle, the lower (Al,Ga)N layer 3 could be p-doped and the upper (Al,Ga)N layer 5 could be n-type doped.

As shown in Figure 1, one example of a suitable n-type dopant is Si, and one example of a suitable p-type dopant

WO 2004/019392

PCT/JP2003/010420

- 12 -

is Mg, although other dopants may be used.

Figure 2 illustrates a growth method of the present invention as applied to the growth of a semiconductor layer structure, in this example to the growth of an LED having the general structure shown in Figure 1. The invention provides a MBE growth method that uses ammonia as the source of nitrogen for the growth process.

According to the present invention, the first (Al,Ga)N layer is grown over the substrate by MBE using ammonia as a nitrogen precursor. This is step 13 in Figure 2, and is carried out with the substrate at a first selected temperature. The substrate is then cooled to a second selected substrate temperature, which is suitable for the growth of the InGaN quantum well region 4. This is step 14 in Figure 2. The (In,Ga)N multiple quantum well structure 4 is then grown at the new substrate temperature, again by MBE using ammonia as the nitrogen precursor. This is step 15 in Figure 2, and steps 15a, 15b, and 15c correspond respectively to the growth of the layers 4a, 4b and 4c shown in Figure 1. The supply of ammonia is maintained continuously during steps 13, 14 and 15, although the flow rate of ammonia may be varied between step 13 and step 14,

WO 2004/019392

PCT/JP2003/010420

- 13 -

or between step 14 and step 15.

A full description of the growth method of Figure 2 will now be given.

5

Initially, a suitable substrate 2 is prepared and cleaned in any conventional manner, and is then introduced into the growth chamber of a suitable MBE apparatus. As mentioned above, the substrate 2 may be a bulk substrate of GaN or AlGaN, or it may consist of an epitaxial layer of GaN disposed on a base substrate.

10

At step 11 the substrate is heated to a pre-determined first substrate temperature T1. According to the present invention, this first substrate temperature is within the range 850°C -1050°C.

15

GaN tends to decompose when it is heated to a temperature of around 800°C or higher. This decomposition may be prevented by supplying an over-pressure of nitrogen gas or ammonia gas to the surface of the GaN. If the substrate 2 is a GaN substrate, or an epitaxial layer of GaN disposed on a base substrate, it is therefore preferable to supply ammonia gas to the growth chamber during step 11 of increasing

20

WO 2004/019392

PCT/JP2003/010420

- 14 -

the substrate temperature, in order to prevent thermal decomposition of the substrate. Since the subsequent growth steps will be carried out using ammonia as the nitrogen precursor, it is preferable if ammonia gas, rather than
5 nitrogen gas, is supplied to the growth chamber during step 11. In principle, the ammonia gas does not need to be supplied at substrate temperatures substantially below 800°C, but it may be more convenient to supply ammonia gas for the entire duration of the heating step.

10

The rate at which the temperature of the substrate is increased to the desired first substrate temperature T1 in step 11 should not be so great that an uneven temperature distribution might occur in the substrate, since this would
15 set up thermal stresses in the substrate. A temperature ramp rate in the range of 10-120°C per minute has been found to be suitable.

Once the substrate has reached the first desired
20 substrate temperature - which is the desired substrate temperature for the growth of the first (Al,Ga)N layer 3, the substrate is preferably maintained at this temperature for up to thirty minutes to bake the substrate and thereby remove contaminants from the substrate. This is step 12 of

WO 2004/019392

PCT/JP2003/010420

- 15 -

Figure 2. The supply of ammonia gas to the growth chamber is maintained through step 12 - indeed, the supply of ammonia gas to the growth chamber is maintained continuously throughout the growth of the layers of the LED 1.

5

At the end of the baking step (or, if there is no baking step, once the temperature of the substrate 2 has reached the desired first substrate temperature) the supply of gallium to the growth chamber is then started. This occurs at time t_1 in Figure 2. If the (Al,Ga)N layer 3 has a non-zero aluminium mole fraction, the supply of aluminium to the growth chamber is also started at time t_1 . Finally, the supply of a suitable dopant to the growth chamber may also be started at time t_1 if the layer 3 is desired to be a doped layer. For example, if the layer 3 is desired to be an n-type doped layer the supply of a suitable n-type dopant such as silicon to the growth chamber is started at time t_1 . Thus, gallium and nitrogen, and possibly a dopant and/or aluminium, are now being supplied to the growth chamber and, as a result, the (Al,Ga)N layer 3 will now be grown by MBE on the substrate 2. Growth of the n-type (Al,Ga)N layer 3 is shown as step 13 in Figure 2, and is maintained for the time required for the layer 3 to reach its desired thickness.

10

15

20

WO 2004/019392

PCT/JP2003/010420

- 16 -

After a growth period corresponding to the time required for the layer 3 to grow to its desired thickness, the supply of gallium, and the supply of the dopant and/or aluminium if these were supplied during step 13, are stopped.

5 This occurs at time t_2 in Figure 2.

The preferred growth temperature for the (In,Ga)N multiple quantum well structure 4 is less than the preferred growth temperature for the (Al,Ga)N layer 3. Once the layer

10 3 has been grown, therefore, the substrate temperature is reduced to a second pre-determined value T2 that is suitable for the growth of the (In,Ga)N multiple quantum well structure 4. This is shown as step 14 in Figure 2. As with step 1,

15 the rate at which the temperature of the substrate is decreased to the desired second substrate temperature T2 should not be so great that an uneven temperature distribution might occur in the substrate. A temperature ramp rate in the range of 10-120°C per minute has been found suitable for the cooling

step.

20

Once the substrate temperature has reached its desired second value, the multiple quantum well structure 4 may then be grown. In order to do this, the supply of gallium to the growth chamber is turned on again at time t_3 , to initiate

WO 2004/019392

PCT/JP2003/010420

- 17 -

the growth of the first (In,Ga)N layer 4a. If the layer 4a is a GaN layer it is sufficient to turn on the supply of gallium at time t_3 - but if the layer 4a has a non-zero indium content it is also necessary to commence the supply of indium to the growth chamber at time t_3 . Gallium and nitrogen, and possibly indium, are now being supplied to the growth chamber and the result is the epitaxial growth by MBE of a GaN or InGaN layer 4a over the first (Al,Ga)N layer 3. This is step 15a shown in Figure 2. The duration of step 15a is chosen to allow the (In,Ga)N layer 4a to grow to its desired thickness. The indium flux during the growth of the first (In,Ga)N layer 4a is selected to obtain the desired indium mole fraction for the layer and, as noted above, if layer 4a is desired to be a GaN layer indium is not supplied to the growth chamber during step 15a.

Once the first (In,Ga)N layer 4a has grown to its desired thickness, the supply of indium to the growth chamber is increased, at time t_4 . If the layer 4a is a GaN layer, the indium flux is turned on at time t_4 , whereas if the layer 4a has a non-zero indium mole fraction then the indium flux is increased at time t_4 from a value appropriate for the indium mole fraction of the first layer 4a of the multiple quantum well structure to a value appropriate for the desired indium

WO 2004/019392

PCT/JP2003/010420

- 18 -

mole fraction of the second layer 4b of the multiple quantum well structure. The second (In,Ga)N layer 4b is then grown, and this is step 15b of Figure 2. The duration of this step is chosen to allow the InGa_N layer 4b to grow to its desired
5 thickness.

Once the InGa_N layer 4b has grown to its desired thickness, the supply of indium to the growth chamber is reduced at time t_5 to a value appropriate to the desired indium
10 concentration of the third (In,Ga)N layer 4c. If the layer 4c is a GaN layer then the supply of indium is completely shut off at time t_5 - however, if the layer 4c has an indium mole fraction that, while lower than the indium mole fraction of the layer 4b, is non-zero, then the indium supply rate
15 is reduced but not stopped at time t_5 . The third (In,Ga)N layer 4c is then grown, and the growth of this layer is step 15c in Figure 2. Once the third (In,Ga)N layer 4c has reached its desired thickness, the supply of gallium, and also the supply of indium if the layer 4c has a non-zero indium mole
20 fraction, are stopped. This occurs at time t_6 in Figure 2.

The gallium flux is preferably held at a substantially constant value over the entire duration of the growth of the (In,Ga)N quantum well structure 4. The composition of

WO 2004/019392

PCT/JP2003/010420

- 19 -

the particular layer being grown is selected by varying the indium flux. In this embodiment, the indium flux may be simply varied between zero ("OFF") and a pre-set value ("ON") so that a sequence of alternating GaN and InGaN layers is grown. Alternatively, the indium flux may be simply varied between a low value and a higher value, so that a sequence of InGaN layers of alternating low and high indium concentrations is grown. Maintaining the gallium flux constant over the growth of the (In,Ga)N quantum well structure 4 has the advantage that the growth rate is also kept constant, since the growth rate of an (In,Ga)N layer is, at low rates of supply of indium, determined primarily by the rate of supply of gallium. Maintaining a substantially constant supply rate of gallium throughout the growth of the multiple quantum well structure 4 therefore ensures that each layer of the quantum well structure 4 is grown at the same growth rate, and this improves the growth quality of the quantum well structure 4.

As noted above in the description of Figure 1, the multiple quantum well structure 4 may contain more than three (In,Ga)N layers. In this case, steps 15b and 15c are repeated as often as necessary to grow the desired multiple quantum well structure.

WO 2004/019392

PCT/JP2003/010420

- 20 -

In the embodiment of Figure 1, the substrate temperature is not intentionally varied during step 15, so that the (In,Ga)N layers 4a, 4b and 4c are grown at the same nominal substrate temperature. Growing the (In,Ga)N layers at the same nominal substrate temperature avoids the need to interrupt the growth process to heat or cool the substrate between growth of one (In,Ga)N layer and growth of the next (In,Ga)N layer.

10

It would alternatively be possible to vary the substrate temperature during the growth of the (In,Ga)N multi layer structure, so that growth of the In-rich layer(s) takes place at a different growth temperature than the growth of the In-poor layers. For example, the indium-poor layers may be grown in a temperature in the range 650°C to 1,000°C, whereas the indium-rich layer(s) may be grown at a temperature within the range 650°C to 800°C. This is because the quality of the indium-poor layers might be better at a higher substrate temperature, and because the incorporation of indium into an indium-poor layer is less critical so that a low growth temperature is not required.

15
20

Once the multiple quantum well structure 4 has been

WO 2004/019392

PCT/JP2003/010420

- 21 -

grown, the temperature of the substrate is then increased to a third pre-determined value T3, in preparation for the growth of the second (Al,Ga)N layer 5. This is step 16 in Figure 2. As for the steps 11 and 14, the rate of change of the temperature of the substrate should not be so great that an uneven temperature distribution might occur in the substrate. A temperature ramp rate in the range of 10-120°C has been found to be suitable.

10 The third desired substrate temperature T3, for the growth of the upper (Al,Ga)N layer 5, is preferably in the range of 850-1050°C. The third substrate temperature T3 is not required to be the same as the first substrate temperature T1, although T1 and T3 may be approximately equal to one
15 another.

Once the substrate has reached the desired temperature T3 for the growth of the second (Al,Ga)N layer 5, the supply of gallium to the growth chamber is started, at time t₇. If the layer 5 has a non-zero aluminium mole fraction, the supply of aluminium to the growth chamber is also started at time t₇. Finally, the supply of a suitable dopant to the growth chamber may also be started at time t₇ if the (Al,Ga)N layer 5 is desired to be a doped layer.

WO 2004/019392

PCT/JP2003/010420

- 22 -

In the example of Figure 1, in which the layer 5 is doped with magnesium, the supply of magnesium to the growth chamber is started at time t_7 . Thus, gallium, and possibly a dopant and/or aluminium, are now being supplied to the growth chamber.

5 As a result, a layer 5 of GaN or AlGaN will be grown by MBE over the multiple quantum well structure 4. This is step 17 in Figure 2. Growth of the (Al,Ga)N layer 5 is maintained until the layer has reached a desired thickness.

10 After a growth period corresponding to the time required for the layer 5 to grow to its desired thickness, the supply of gallium, dopant (if present) and, in the case of an AlGaN layer, aluminium to the growth chamber is then shut off. This occurs at time t_8 in Figure 2. The light

15 emitting diode structure 1 shown in Figure 1 is now complete.

Finally, in step 18, the substrate is cooled to room temperature. As for the other heating or cooling steps, the rate of change of the substrate temperature should not be

20 so great that an uneven temperature distribution might occur in the substrate. A temperature ramp rate in the range of 10-120°C per minute should again be suitable.

The supply of ammonia to the growth chamber is

WO 2004/019392

PCT/JP2003/010420

- 23 -

preferably maintained until the substrate temperature is substantially below 800°C, to prevent thermal decomposition of the (Al,Ga)N layer 5. The supply of ammonia to the growth chamber may be maintained for the entire duration of the

5 cooling step 18 for convenience.

Ammonia gas is supplied to the growth chamber continuously during steps 12 to 17. Furthermore, ammonia is preferably supplied during the initial heating stage 11

10 and the final cooling stage 18, except perhaps for substrate temperatures significantly below 800°C. The ratio of the ammonia to the elemental metal supplied to the growth chamber is preferably in the range 10:1 to 10,000:1. This high V/III ratio allows the GaN and InGaN layers to be grown by MBE

15 at temperatures well above those used in prior MBE growth methods, and this leads to improved material quality.

The beam equivalent pressure of ammonia gas is preferably in the range from 1×10^{-4} to 2×10^{-2} mbar. In

20 steps 13 and 17 of growing the (Al,Ga)N layers 3, 5 the beam equivalent pressure of elemental gallium, or of elemental gallium and aluminium, is preferably within the range 1×10^{-8} to 1×10^{-4} mbar (where both elemental gallium and aluminium are supplied, the total beam equivalent pressure of the

WO 2004/019392

PCT/JP2003/010420

- 24 -

gallium and aluminium is preferably within this range).

In the growth of the (In,Ga)N layers 4a, 4b, 4c, the beam equivalent pressure of elemental gallium, or of
5 elemental gallium and indium, is preferably within the range 1×10^{-8} to 1×10^{-4} mbar (again, where both elemental gallium and indium are supplied their total beam equivalent pressure is preferably within this range).

10 In an embodiment in which magnesium is used as the p-type dopant for the second (Al,Ga)N layer 5, the beam equivalent pressure of magnesium during step 17 is preferably within the range from 1×10^{-9} to 1×10^{-7} mbar.

15 In the embodiment described above the (In,Ga)N layers 4a, 4b, 4c of the multiple quantum well structure were not intentionally doped. It would alternatively be possible for one or more of these layers to be intentionally doped, for example to be doped n-type. These layers may be doped n-type
20 by supplying a suitable n-type dopant, for example such as silicon, to the growth chamber during step 15a, 15b and/or 15c, as appropriate.

Figure 3 shows an electro-luminescence spectrum of

WO 2004/019392

PCT/JP2003/010420

- 25 -

a light-emitting diode having the general structure shown in Figure 1 grown by a MBE growth method of the present invention. It will be seen that the electro-luminescence spectrum has a well-defined peak centred on approximately 405nm wavelength, and this is indicative of good crystal quality.

It should be noted that the use of MBE to grow the p-type doped (Al,Ga)N layer has the advantage that the layer has a good carrier concentration, and there is no need to anneal the dopant. Where such a layer is grown by MOVPE, it is necessary to anneal the layer to activate the dopant in order to maintain a satisfactory concentration.

The invention has been described above with reference to growth of an LED structure in which the (In,Ga)N quantum well structure comprises three adjacent (In,Ga)N layers. The invention is not limited to the growth of this particular structure, however, and may be applied generally to the growth of a semiconductor layer structure having an (In,Ga)N quantum well structure, containing one or more (In,Ga)N layers, disposed between first and second (Al,Ga)N layers.

The present invention requires a MBE growth apparatus

WO 2004/019392

PCT/JP2003/010420

- 26 -

that can achieve a V/III ratio of more than 10:1 during the growth process, and that can preferably achieve a V/III ratio of more than 500:1 during the growth process. The use of a high V/III ratio during the growth process of the present invention allows an InGaN nitride semiconductor layer to be grown at temperatures well above those used in prior art MBE methods. This leads to improved material quality. The MBE growth process of the present invention requires at least one thousand times less ammonia gas than does a conventional MOVPE process.

Such high V/III ratios can be achieved, for example, in a MBE growth apparatus in which ammonia gas is introduced into the growth chamber through a conduit whose outlet end is placed as close to the substrate as possible without radiative heat from the substrate causing excessive local heating of the outlet of the supply conduit. The elemental gallium and elemental indium can be introduced into the growth chamber using a conventional effusion cell. Further effusion cells can be used to supply aluminium and/or an elemental dopant for incorporation into the epitaxial growth material as necessary.

Figure 4 is a schematic view of an apparatus suitable

WO 2004/019392

PCT/JP2003/010420

- 27 -

for the growth of a nitride semiconductor material by molecular beam epitaxy according to a method of the present invention. The apparatus comprises a growth chamber 10 in which is disposed a heated support 6 arranged to support and heat a substrate S. The growth chamber 10 is connected with an ultra-high vacuum pump 7 via an exhaust conduit 8 which extends into the growth chamber 10. The inner end of the exhaust conduit 8 defines a vacuum outlet 9 of the growth chamber 10. The vacuum outlet 9 is disposed adjacent to the substrate support 6.

The growth chamber 10 is further provided with a first supply conduit 20 which extends into the growth chamber so that an outlet 22 of the first supply conduit 20 is adjacent to and faces the surface of the substrate S upon which epitaxial growth is to take place. The first supply conduit 20 can be adjustably mounted relative to the chamber so that the relatively small distance between the outlet 22 of the first supply conduit 20 and the epitaxial growth surface of the substrate S can be varied during the epitaxial growth process. The longitudinal axis of the first supply conduit 20 is substantially perpendicular to the plane of epitaxial growth.

The first supply conduit 20 is used to supply ammonia

WO 2004/019392

PCT/JP2003/010420

- 28 -

which is the precursor of the nitrogen required in the epitaxial growth process. Because the outlet 22 of the first supply conduit 20 is positioned relatively close to the substrate S, a relatively high ammonia vapour pressure is localised at the surface of the epitaxially growing material while still enabling an ultra-high vacuum environment within the growth chamber 10 to be achieved by the pump 7. The high ammonia vapour pressure enables a high V/III ratio to be realised during the growth process.

10

The apparatus further comprises independently operable, shutter-controlled effusion cells 24, 26 (two such cells are shown in Figure 5) which contain sources of elemental gallium and for the epitaxial growth process. The effusion cells 24 and 26 are conventionally positioned and define second and further supply conduits respectively. Additional effusion cells may be provided to supply, for example, dopant species during the growth process.

20

A MBE apparatus of the type described above is described in European Patent Application No. 98301842.5, the contents of which are hereby incorporated by reference. It should be noted, however, that the present invention is not limited to a MBE apparatus of the type described in European

WO 2004/019392

PCT/JP2003/010420

- 29 -

Patent Application No. 98301842.5/0 864 672, but can be carried out in any MBE growth apparatus that can provide the required V/III ratio.

5

INDUSTRIAL APPLICABILITY

According to the present invention, high quality semiconductor layer structures comprising one or more $\text{In}_x\text{Ga}_{1-x}\text{N}$ ($0 \leq x \leq 1$) layers can be grown by molecular beam epitaxy (MBE). The present invention achieves improved material quality over conventional MBE growth methods and requires at least one thousand times less ammonia gas than a conventional metal-organic vapour phase epitaxy growth. These semiconductor layer structures are suitable for use in light-emitting diodes and laser diodes, for example. The growth of these layer structures is therefore of considerable commercial importance.

WO 2004/019392

PCT/JP2003/010420

- 30 -

CLAIMS

1. A method of growing a semiconductor layer structure, the
5 method comprising the steps of:

growing a first (Al,Ga)N layer over a substrate at
the first substrate temperature by MBE using ammonia as the
nitrogen precursor;

10 cooling the substrate to a second substrate
temperature lower than the first substrate temperature, while
maintaining the supply of ammonia to the substrate;

growing an (In,Ga)N quantum well structure over the
first (Al,Ga)N layer by MBE using ammonia as the nitrogen
precursor;

15 heating the substrate to a third substrate
temperature higher than the second substrate temperature,
while maintaining the supply of ammonia to the substrate;
and

20 growing a second (Al,Ga)N layer over the quantum well
structure at the third substrate temperature by MBE using
ammonia as the nitrogen precursor.

2. A method as claimed in claim 1 wherein the first (Al,Ga)N
layer has a first conductivity type.

WO 2004/019392

PCT/JP2003/010420

- 31 -

3. A method as claimed in claim 2 wherein the second (Al,Ga)N layer has a second conductivity type different from the first conductivity type.

5

4. A method as claimed in claim 3 wherein the first (Al,Ga)N layer is doped n-type and the second (Al,Ga)N layer is doped p-type.

10 5. A method as claimed in any preceding claim wherein the first substrate temperature is within the range 850°C to 1050°C.

15 6. A method as claimed in any preceding claim wherein the second substrate temperature is within the range 650°C to 1000°C.

20 7. A method as claimed in any preceding claim wherein the third substrate temperature is within the range 850°C to 1050°C.

8. A semiconductor layer structure grown by a method as defined in any of claims 1 to 7.

WO 2004/019392

PCT/JP2003/010420

- 32 -

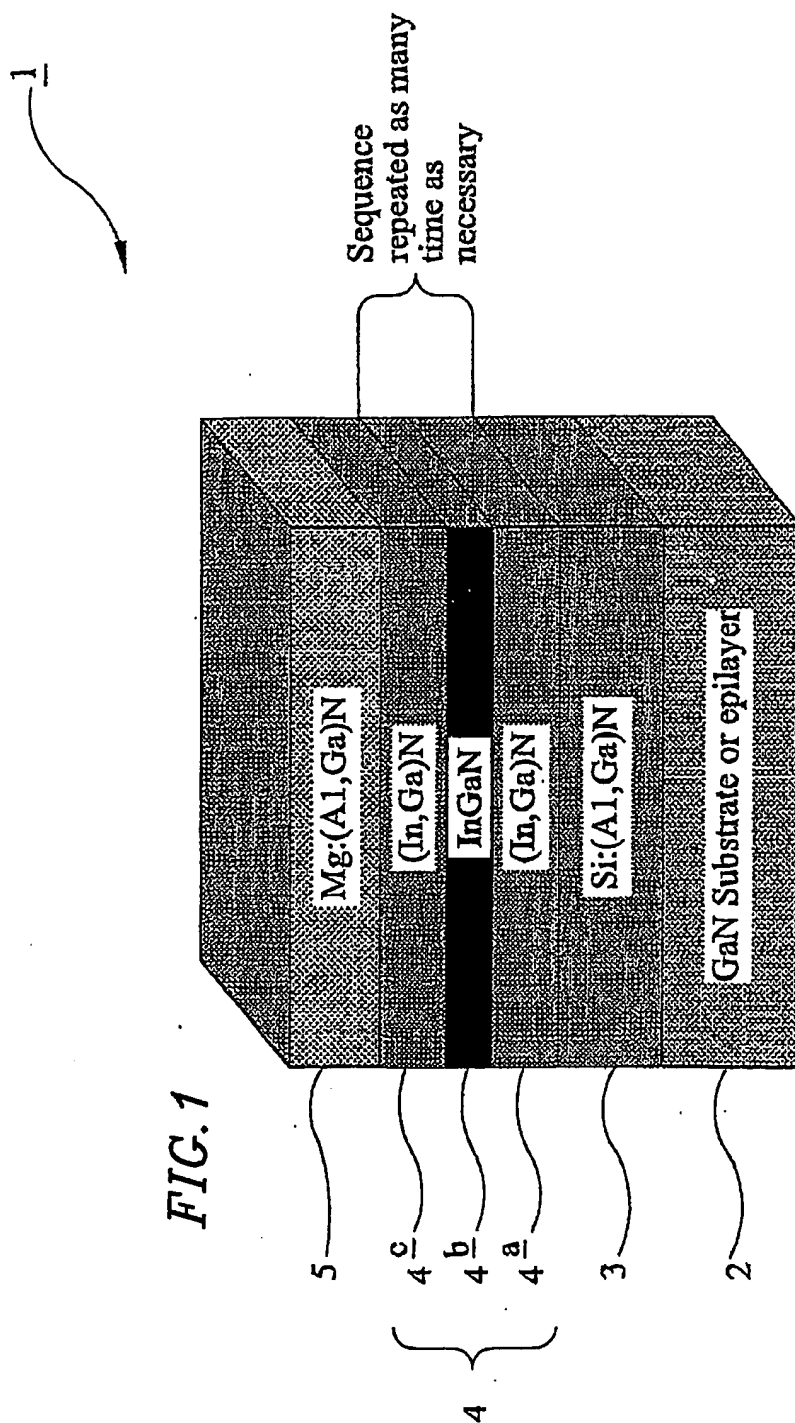
9. A semiconductor light-emitting device comprising a semiconductor layer structure grown by a method as defined in any of claims 1 to 7.

- 5 10. A semiconductor device as claimed in claim 9 wherein the device is a light-emitting diode.

WO 2004/019392

10/525499
PCT/JP2003/010420

1 / 4



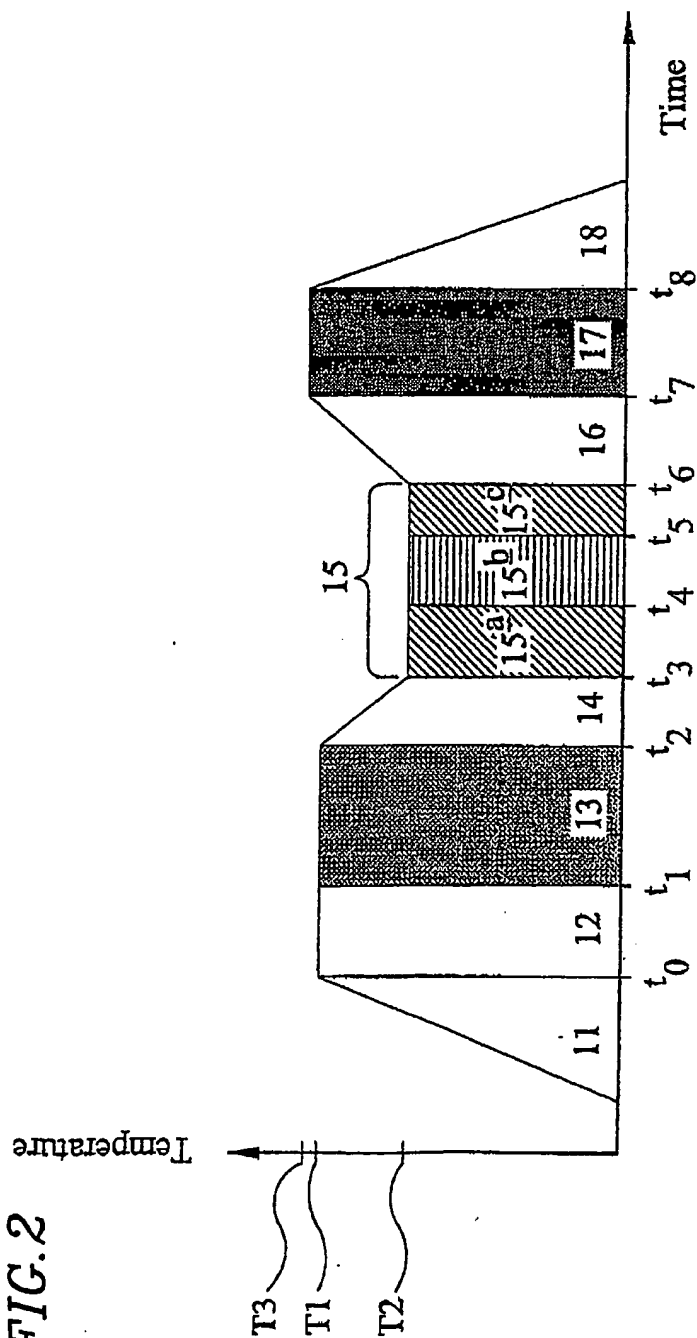
WO 2004/019392

PCT/JP2003/010420

10/525499

2 / 4

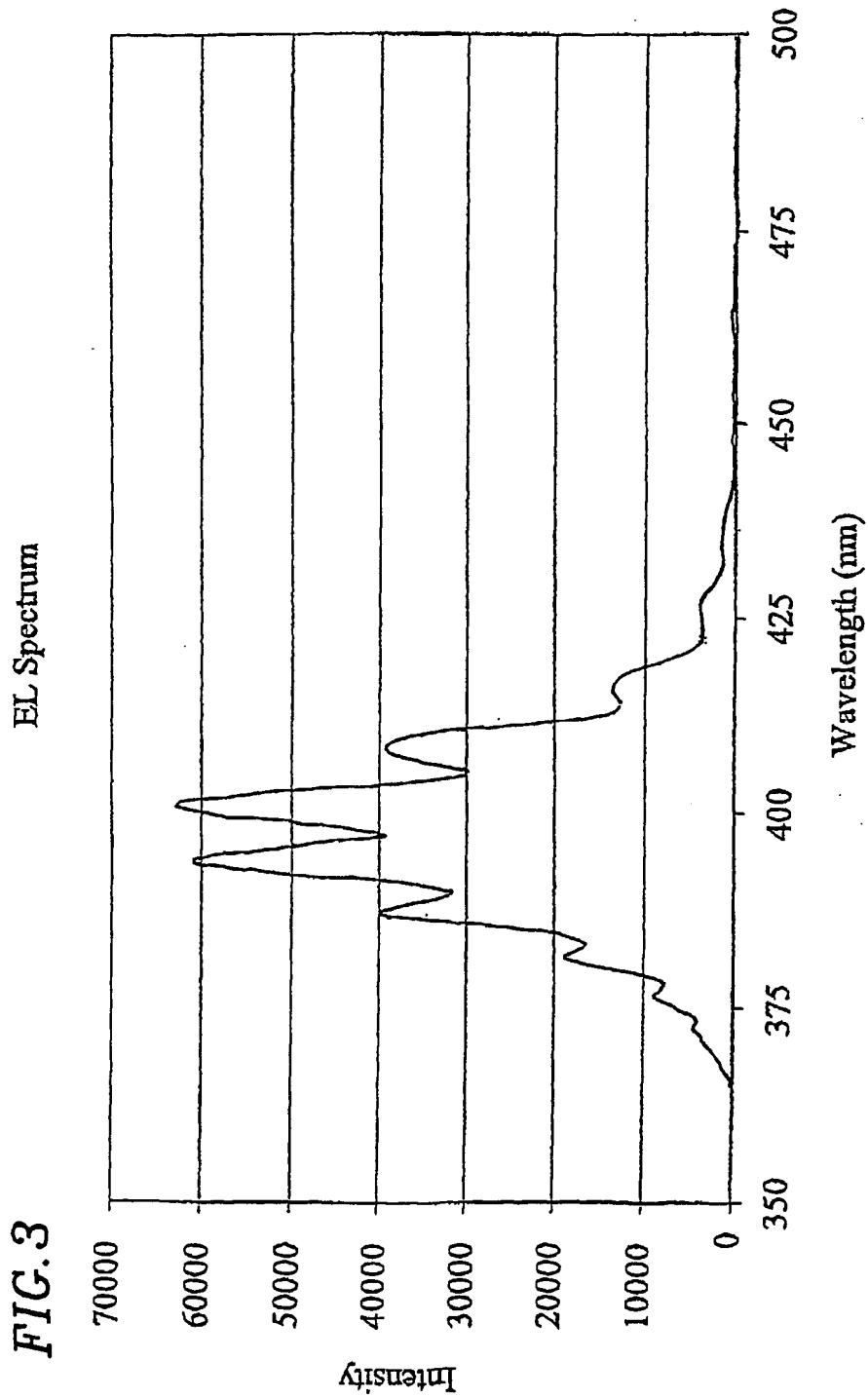
FIG. 2



WO 2004/019392

10/525499
PCT/JP2003/010420

3 / 4



WO 2004/019392

10/525499
PCT/JP2003/010420

4 / 4

